Artyom Lazyan

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Experience

Software Engineer
Xilinx

Apr 2020 - Present (1 year +)

Research and develop novel algorithms and techniques for Floorplanning.
Placement and Routing with the aim of improving Quality of Results (QoR)
Runtime and Memory, of Xilinx's physical design software tools by orders of magnitude.
Evaluate new FPGA architecture features and their impact on existing EDA tools.

Junior Research Scientist
IIAP (Institute for Informatics and Automated Problems)

Jun 2020 - Present (10 months +)

Graph theory, self-organized criticality.
monte carlo simulations, data analysis, machine learning, parallel programming.

R&D Engineer, I
Synopsys Inc

Jan 2019 - Apr 2020 (1 year 4 months)

-Programming with C/C++
-Responsible for designing, developing, debugging and troubleshooting software programs
-Performing engineering and regression testing to verify software quality
-Enhancing and optimizing existing program functionality
-Fixing bugs sometimes by applying debugging tools for finding the bottlenecks (especially by using Total View, GDB and Valgrind)

R&D Engineer (Intern)
Synopsys Inc

Apr 2018 - Jan 2019 (10 months)

Intern (Technical Engineering) at Synopsys Yield Explorer gui team
-Programming with C++
-GUI development using Qt library,
-Performing engineering and regression testing to verify software quality

Education

Synopsys Armenia Educational Department
Master of Engineering - MEng, Electronic Design Automation

2019 - 2021

Synopsys Armenia Educational Department

Bachelor of Engineering, Electronic Design Automation

2017 - 2019

National Polytechnical University of Armenia

Bachelor of Technology, Computer Engineering

2013 - 2019

Skills

GUI development • Research and Development (R&D) • Debugging and troubleshooting software programs • C (Programming Language) • C++ • Python (Programming Language) • Git • JIRA • Qt • Qt Creator